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Assitant, CASYS@KAIST, Daejeon. Mar. 2019- Dec. 2019 Undergraduat Mar 12th, 2024.

KYUIL(Bernard) HWANG6. Xilinx FPGA Low Level Design And Floor Planning With VIVADO And Spartan Amazon Lab126 / Cupertino And Sunnyvale Sept 2013 – Aug 2015 RF System Hardware Engineer 1. Wireless Area Networ Jan 11th, 2024

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