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Course W.e.f. Academic Year 2019-20 And Onwards
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ELECTRONICSTo Implement A Half Adder Using Basic
Gates To Implement A Half Subtractor Using Universal
Gate (NAND) Equipment The Equipment You Require Is
As Follows: IC 7404, IC 7400, IC 7483 A Half Adder Is A

Logical Circuit That Performs An Addition Operation On Two One-bit Binary Numbers Often Written As A And B. 3th, 2024

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5. Write A Program To Draw A Circle Using Bresenham's Algorithm.
6. Write A Program To Draw A Rectangle Using Line Drawing Algorithm.
7. Write A Program To Perform 2D Transformation On A Line.
8. Write A Program To Perform Shear Transformation On A Rectangle.
9. Write A Program To Rota 1th, 2024.

Computer Graphics - Computer Science - Computer Science Animation E. Virtual Reality Computer Graphics B. Bit-mapped Graphics

1. The Photoshop Images We Manipulated Were Composed Of Pixels
2. This Type Of Graphic Is Called Bit-mapped Or Raster Graphics And Is Pixel-oriented
3. Graphic Laid Out Over A Coordinate (X Y) System
- Graphic Laid Out 2th, 2024

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University. Kathleen Lynch-Davis: 4th, 2024 Computer Graphics Lecture Notes - Dynamic Graphics Project T. The Set Of Real Numbers Is Represented By R . The Real Euclidean Plane Is R^2 , And Similarly Eu- ... - So For Any Point P on The Line, $(\vec{p}-\vec{p}_0)\cdot\vec{n} = 0$. Here $\vec{n} = \dots$ An Important Case In The Previous Section 1th, 2024.

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