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SM 3 EECE 488 – Set 1: Introduction And Background References • Main Reference: Lecture Notes • Recommended Textbook: Behzad Razavi, Design Of Analog CMOS Integrated Circuits, McGraw- Hill, 2001 • Some Other Useful References: Mar 9th, 2024

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EECE488 Analog CMOS Integrated Circuit Design Assignment 4 Due: Tuesday November 26th, 2013 At 8:00am 1. Consider The Following Circuit. (This Circuit Is Sometimes Referred To As A Self-biased Current Source). R V DD GND M 0 M 2 M 1 M 3 GND 4 GND I Out Assume All Transistors Are Operating In Saturation Region And $V_{GS} = 0$, $V_{DD} = 3V$, $V_{TH}(NMOS) = 0$... Apr 4th, 2024

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