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Department Of Electronics And Communication M.Tech In ... 1 Introduction To Embedded Systems Shibu K V Tata McGraw Hill Education Private Limited, 2009 . SRI SIDDHARTHA INSTITUTE OF TECHNOLOGY- TUMAKURU (A Constituent College Of Siddhartha Aca 10th, 2024 Department Of Electronics And Communication Engineering ... EMBEDDED SYSTEMS LAB MANUAL (On ARM Microcontroller And RTOS) I Year M.Tech I - Sem Prepared By Ch. Madhuri Devi Asso.Prof Easari. Parushuramu Asst.Proff Under Prof. K. Ashok Babu HOD Of ECE Dept. SriIndu College Of Engineering And Technology (an Autonomous Institution Under JNTUH) S 2th, 2024 DEPARTMENT OF ELECTRONICS AND COMMUNICATION Full Adder Using (i) Basic Logic Gates And (ii) NAND Gates. (b) Full Subtractor Using (i) Basic Logic Gates And (ii) NAND Gates. 3 Design And Implement 4-bit Parallel Adder/ Subtractor Using IC 7483. 4 Design And Implementation Of 5-bit Magnitude Comparator Using IC 7485. Realize 5 (a) Adder ... 8th, 2024.

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