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Details Of The Bitcoin Mining Process On Section 3. Then We Outline Our FPGA Implementation Of A Bitcoin Miner On Section 4. And Finally On Section 5 We Conclude And Present Some Future Work. 2. Bitcoin N Jan 1th, 2024

FPGA Cryptocurrency Miner

• Bitcoin Block Header Relatively Constant – Only Changes Per Block Are Nonce Incrementation (potentially 2. 32. Attempts Per Block) • Hash Merkle Root (assumed Constant Here) Represents Transactions To Validate. FPGA Implementation - SDII. Figure 3 – High Level Block Diagram Of An FPGA Crypto Jan 2th, 2024

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