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Circuit Shown In Figure 1.11, The Voltage Source Is In Series With  $R_1$  And The Current Source Is In Parallel With  $R_4$ . None Of The Three Resistors Is Either In Series Or In Parallel With Any Other Resistor.  $R_1 R_2 R_3$  Vs I  $R_4$  S Figure 1.11: Jan 27th, 2024.

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