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Driver Fault Signal, Which Indicates That At Least One Fault Event Has Occurred. The Phase Voltage Signals Enter The LPFs. Their Cutoff Frequency Is Three Times Higher Than The Phase Switching Frequency Generated By The Motor Driver. The PSoC Analog Blocks Process The Phase Voltages. As Mentioned Above, PSoC's AGND Is Floating And Mar 2th, 2024

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Ethernet Control AC Motor Via PLC Using LabVIEW

OPC Quick Client So That The OPC Tags Can Be Browsed By The Shared Variables In This VI. There Are 5 OPC Tags Was Created And Used In The Implementation, And The Details Of The Tags Have Been Tabulated In Table I. TABLE I . THE DETAILS OF OPC TAG Jul 2th, 2024

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Communication Protocol For Connecting Slaves With This Communication Protocol. Port2 Can Provide FATEK High Speed CPU Link Function. Extended Usage • Under Standard I Terf C E, As The L V F Ormul I-dr OpFATEK R485 Oi Int 232 CP LINK Etwork. • Under L Adder Di Gr Mpr Ocntr Lled In Terf Eyp S, Port1~Por 4 H She Fllow Ing Unct On : May 2th, 2024

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C0-00dr-d 120 0 C0-00ar-d C0-01dd1-d 140 60 C0-01dd2-d C0-01dr-d 140 0 C0-01ar-d C0-02dd1-d 140 60 C0-02dd2-d 140 0 C0-02dr-d C0-10dd1e-d 120 ... C0-11dre-d C0-11are-d 8 Ac Rs-485 + _ Lg C0-11dd1e-d C1 X1 X2 X3 X4 C2 X5 X6 X7 X8 C3 Y1 Y2 Y3 Y4 C4 Y5 Y6 +v Lnk/act Port1 Pwr Run Err Run Stop P Jul 1th, 2024

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