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Co-Design Of Deep Neural Nets And Neural Net Accelerators ...
Co-Design Of Deep Neural Nets And Neural Net Accelerators For Embedded Vision Applications
Kiseok Kwon,^{1,2} Alon Amid,¹ Amir Gholami,¹ Bichen Wu,¹ Krste Asanovic,¹ Kurt Keutzer¹
¹ Berkeley AI Research, University Of California, Berkeley ² Samsung Research, Samsung Electronics, Seoul, South Korea {kiseo
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James A. Weston,^{1*} Hisahiro Yoshida,² Victoria Robinson, Satomi Nishikawa,² Stuart T. Fraser,² And Shinichi Nishikawa³
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Values Of Z Is 1 Rather Than Very Close To 0. 7.2 The XOR Problem
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