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PHY Interface For The PCI Express* Architecture PCI Express 3PHY Functions Which Must Be Incorporated In A PIPE Compliant PHY, And It Defines A Standard Interface Between Such A PHY And A Media Access Layer (MAC) & Link Layer ASIC. It Is Not The Intent Of This Specification To Define The Internal Arc 4th, 2024PCI-104-Express-FBxx PCIe-104-FBxx PCI-104-FBxx PCIe-104 ...Physical Characteristics - PCI-104-Express-FBxx, PCIe-104-FBxx, PCI-104-FBxx STEP Model Is Available Upon Request; Contact RTD Tech Support For More Information. Weight: Approximately 55 G (0.12 Lbs.) Dime 2th,

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