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HDL Description Into An Internal Gate-level Equivalent.  
Before Using This Manual, You Should Be Familiar With  
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Verilog Is A Hardware Design Language That Provides  
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31**

Register; Verilog Code For Generic N . Verilog Code For  
Serial In Parallel Out Shift Register Verilog Code For  
Serial In Parallel Out Shift .. 25 26 27 28 29 30 31 32  
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Shown Any Code For Shift Or For . Vlsi World: Verilog  
Codes For Different Shift-registers Verilog Code For An  
8-bit Shift-left Register With A Positive-edge Clock,  
Serial In And Serial Out.. Module Shift (clk, Si, So);  
Input Clk,si . 1th, 2024

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