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Dr. C V Srinivasa, Department Of Civil Engineering Global Academy Of Technology,  
RR Nagar, Bengaluru-560098 Svasa@gat.ac.in, 94498 09918 Module 5: Theories Of  
Failure Objectives: The Objectives/outcomes Of This Lecture On “Theories Of  
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Fundamentals Of CMOS VLSI 10EC56 CITSTUDENTS.IN Page- 3 INDEX SHEET SL.NO TOPIC PAGE NO. 1 7UNIT 1: Basic MOS Technology: -44 I N Teg R A D C lu S , E H Ce Mt D Pl On De MOS Transistors 8-16 NMOS F Abr Ic T On 14-16 CMOS Fabr Icat On 17-25 T He Rm A Lspc T Of Ce Ing, B CMOS Ec N Ogy, Production Of E-beam Masks Jun 2th, 2024

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Assume Want To Shift Left By  $K$ ,  $0 \leq K \leq N-1$  ( $N = 2n$ )  $K$  Expressible As  $N$ -bit Number:  $-K = K_{n-1}2^{n-1} + K_{n-2}2^{n-2} + \dots + K_12^1 + K_0$ ,  $K_i$  A 0 Or 1 Barrel Shifter:

Construct From N Levels Of N 2-in Multiplexors - When Level I Either Shifts Last Level By 2 I-1 Or Pass Unchanged Circuits-C Sli Jun 16th, 2024

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3 Advanced VLSI Design CMOS Inverter CMPE 640 Propagation Delay  $R$  Is Equal To The Resistance Ratio Of Identically Sized PMOS And NMOS Transistors:  $R_{eq}/R$  Eqn. The Optimal Value Of  $B$  Can Be Found By Setting When Wiring Cap Feb 22th, 2024

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VLSI Circuits - A Survey 1D.vijayalakshmi, 2Dr P.C Kishore Raja 1Assistant Professor, BIT, Bangalore 2HOD, Department Of E&C, Saveetha University Abstract: This Paper Covers The Various Techniques Used To Jan 9th, 2024

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3 Vdd Gnd Avdd 2 0 B 0 A 1 B 1 A 2 B 2 D. Z. Pan 10. Interconnects In CMOS Technology 23 Repeaters • R And C Are Proportional To L • RC Delay Is Proportional To L<sup>2</sup> - Unacceptably Great For Long Wires • Break Long Wires Into N Shorter



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